



USN 10/719217

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes  
Serial No.: 10/719217  
Filed: November 20, 2003  
Title: PROGRAMMABLE FUSE AND ANTIFUSE AND METHOD THEREFOR

Examiner: Hien Nguyen

Group Art Unit: 2824

Docket: 303.620US2

COMMUNICATION CONCERNING RELATED APPLICATION(S)

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Applicant would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date</u>	<u>Attorney Docket</u>	<u>Title</u>
09/383804 6521958	August 26, 1999	303.588US1	MOSFET TECHNOLOGY FOR PROGRAMMABLE ADDRESS DECODE AND CORRECTION
09/924659 6700821	August 8, 2001	303.588US2	MOSFET TECHNOLOGY FOR PROGRAMMABLE ADDRESS DECODE AND CORRECTION
10/763136	January 22, 2004	303.588US3	PROGRAMMABLE MEMORY CELL USING CHARGE TRAPPING IN A GATE OXIDE

Respectfully submitted,

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By Applicant's Representatives,

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Date May 27, 2004 By Daniel J. Kluth  
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 27 day of May, 2004.

Name

Tina Kowal

Signature

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